



Lecture Session Schedule - Detailed Agenda

	3C	3H+3J	3A	3D	3E	3B	3G	5C	5F	5D	5H	5E	5A	5B
Monday May 25th, 2026 10:30-12:00	A2L-01 Modeling, Sensors, and Emerging Analog Systems	A2L-02 High-Speed Data Converters and Time-Domain ADCs	A2L-03 High-Speed Interface and Wireline Circuits	A2L-04 Hardware Security for Internet-of-Things, Cyber-Physical Systems I	A2L-05 SoC, NoC, Multi-Core, and 3D/2.5D Integrated Circuits and Systems I	A2L-06 Post-Quantum Cryptography and High-Speed Links	A2L-07 Integrated Power Circuits and Charge Pumps	A2L-08 Ferroelectric and Memristive In-Memory Computing	A2L-09 Spiking Neural Network Architectures and Learning	A2L-10 Image and Vision Sensors I	A2L-11 Accelerators for Machine Learning	A2L-12 Representation and Compression for Vision and Multimodal Learning Systems	A2L-13 Synergies between Emerging AI Technologies and Circuits and Systems	A2L-14 AI for Medical Signal and Image Analysis
Monday May 25th, 2026 15:00-16:30	A4L-01 AI-Assisted and Emerging Analog Circuits	A4L-02 Pipeline and Non-Conventional ADC Architectures	A4L-03 Voltage References and Power Management	A4L-04 Datapath & Arithmetic Circuits and Systems I	A4L-05 SoC, NoC, Multi-Core, and 3D/2.5D Integrated Circuits and Systems II	A4L-06 Wireless Communications I	A4L-07 High Efficiency Converters and Drive Circuits for Specialized Applications	A4L-08 Spintronics-based In-Memory Computing	A4L-09 Neuromorphic Circuits and Neuron Implementations	A4L-10 Biochemical and Environmental Sensors	A4L-11 Circuits, Systems and Architectures for Machine Learning I	A4L-12 Compression Techniques for Rendering, Perception, and Forecasting	A4L-13 Artificial Intelligence in Power and Energy Circuits and Systems I	A4L-14 Bio-Signal Acquisition Front-Ends
Monday May 25th, 2026 16:30-18:00	A5L-01 Programmable, High-Drive, and High-Voltage Amplifiers	A5L-02 Delta-Sigma ADCs and DAC Techniques	A5L-03 VCO Design and Oscillator Techniques	A5L-04 Datapath & Arithmetic Circuits and Systems II	A5L-05 Electronic Design Automation and Physical Design I	A5L-06 Wireless Communications II	A5L-07 Circuits and Systems for Wireless Power Transfer applications	A5L-08 Quantum Computing I	A5L-09 Grand Challenge on Neural Network-based Video Coding	A5L-10 Sensory Circuits and Systems I	A5L-11 Circuits, Systems and Architectures for Machine Learning II	A5L-12 Multimodal Dialog, Speech Processing, and 3D Representation	A5L-13 Artificial Intelligence in Power and Energy Circuits and Systems II	A5L-14 Medical Computing Hardware and SoCs
Tuesday May 26th, 2026 10:30-12:00	B2L-01 RF Frequency Generation and Synthesizers	B2L-02 Time-Interleaved and High-Speed SAR ADCs	B2L-03 Oscillators, PLLs, and Clock Generation	B2L-04 Hardware Security for Logic, Circuits and Architectures I	B2L-05 Electronic Design Automation and Physical Design II	B2L-06 Wireline Communications I	B2L-07 Circuits and Systems for Enhanced DC-DC Switch-mode Power Supplies	B2L-08 AI Circuits and Architectures	B2L-09 Compute-in-Memory-Based Neural Computing	B2L-10 Sensory Signal Conditioning	B2L-11 Quantization, Approximation, and Compression for ML Hardware I	B2L-12 Multimodal Interfaces and Efficient Architectures for Intelligent Visual Systems	B2L-13 Empowering the Evolving Electrical Grid: Circuits & Systems for Greener Generation, Distribution I	B2L-14 Neural Recording and Stimulation ASICs
Tuesday May 26th, 2026 15:00-16:30	B4L-01 RF and mm-Wave Low-Noise Amplifiers	B4L-02 Delta-Sigma and Zoom ADCs	B4L-03 Multiphase PLLs and Injection-Locked Clocks	B4L-04 Hardware Security for Logic, Circuits and Architectures II	B4L-05 Electronic Design Automation and Physical Design III	B4L-06 Homomorphic Encryption and Quantum Circuits	B4L-07 Circuits & Systems for Energy Harvesting I	B4L-08 Nanoelectronic Circuits	B4L-09 Neuromorphic Processor Architecture and System	B4L-10 Machine Learning for Signal Processing	B4L-11 Compute-in-Memory I	B4L-12 Computational Intelligence for Multimedia Understanding	B4L-13 Empowering the Evolving Electrical Grid: Circuits & Systems for Greener Generation, Distribution II	B4L-14 Sensing Interfaces and SoCs
Tuesday May 26th, 2026 16:30-18:00	B5L-01 RF and mm-Wave Transceivers and Receivers	B5L-02 Noise-Shaping ADCs and DAC Techniques for SAR ADCs	B5L-03 Advanced Analog and Mixed-Signal Techniques I	B5L-04 Hardware Security for Logic, Circuits and Architectures III	B5L-05 Programmable, Reconfigurable & Array Architectures I	B5L-06 AI/ML for Communication and Signal Processing	B5L-07 Circuits & Systems for Energy Harvesting II	B5L-08 Quantum Computing II	B5L-09 Neuromorphic Learning and Plasticity	B5L-10 Signal Processing Theories and Algorithms for Biosignals	B5L-11 Compute-in-Memory II	B5L-12 Circuits and Systems for Coding and Processing	B5L-13 Specialized Hardware for Embodied AI Application and Neuromorphic Computing I	B5L-14 Wireless Power and Implantable Systems
Wednesday May 27th, 2026 10:30-12:00	C2L-01 RF and mm-Wave Power Amplifiers and Combiner	C2L-02 SAR ADC Calibration and Energy Optimization	C2L-03 Advanced Analog and Mixed-Signal Techniques II	C2L-04 Low-Power Logic, Circuits & Architectures I	C2L-05 Design and Verification of Digital Integrated Circuits and Systems I	C2L-06 CAS Education and Outreach and the Open Silicon Initiative	C2L-07 Modeling, Control, and Power Management	C2L-08 WFoT	C2L-09 Hardware-Aware Neural Networks	C2L-10 Image Processing: Segmentation, Compression, Restoration, Registration, and Enhancement	C2L-11 Conventional and Emerging Memory Circuits and Architectures	C2L-12 Video Coding	C2L-13 Specialized Hardware for Embodied AI Application and Neuromorphic Computing II	C2L-14 Flexible Electronics
Wednesday May 27th, 2026 13:30-15:00	C3L-01 RF Switches, Couplers, and Phase Control	C3L-02 Precision and Low-Noise Amplifiers	C3L-03 Modeling Methods for Nonlinear Circuits and Systems I	C3L-04 Low-Power Logic, Circuits & Architectures II	C3L-05 Design and Verification of Digital Integrated Circuits and Systems II	C3L-06 Breaking Barriers in Privacy-Preserving Machine Learning: From Algorithms to Accelerators	C3L-07 Bridging Algorithms, Circuits, and Systems: Foundations and Applications of Artificial Intelligence	C3L-08 Energy-Efficient & High-Resolution Data Converters for Next-Generation Biomedical & Neural Interface	C3L-09 Neuromorphic Perception and Control	C3L-10 Image Processing: AI in Circuits and Systems	C3L-11 Edge Computing	C3L-12 Learning-based Image/Video Coding	C3L-13 Analysis and Optimization of Complex Systems and Artificial Intelligence Applications I	
Wednesday May 27th, 2026 16:00-17:30			C5L-03 Advanced Circuits and Systems for High-Speed Data Links and Wireless Connectivity	C5L-04 Low-Power Logic, Circuits & Architectures III	C5L-05 Circuits and Systems for Physiological Sign Detection and Risk Warning in the Elderly	C5L-06 Cross-Layer Innovation in Compute-in-Memory Chips	C5L-07 Application-Specific Computer Arithmetic for Post-Quantum Cryptography & Homomorphic Encryption Access	C5L-08 Integrated Circuits and Systems for Intelligent Edge and Biomedical Applications	C5L-09 Quantization, Approximation, and Compression for ML Hardware II	C5L-10 Compressive Sensing and Sparse Signal Processing	C5L-11 Cross-Layer Optimization for Machine Learning	C5L-12 Volumetric video coding and communication	C5L-13 Analysis and Optimization of Complex Systems and Artificial Intelligence Applications II	